

Fig. 1 Prior art

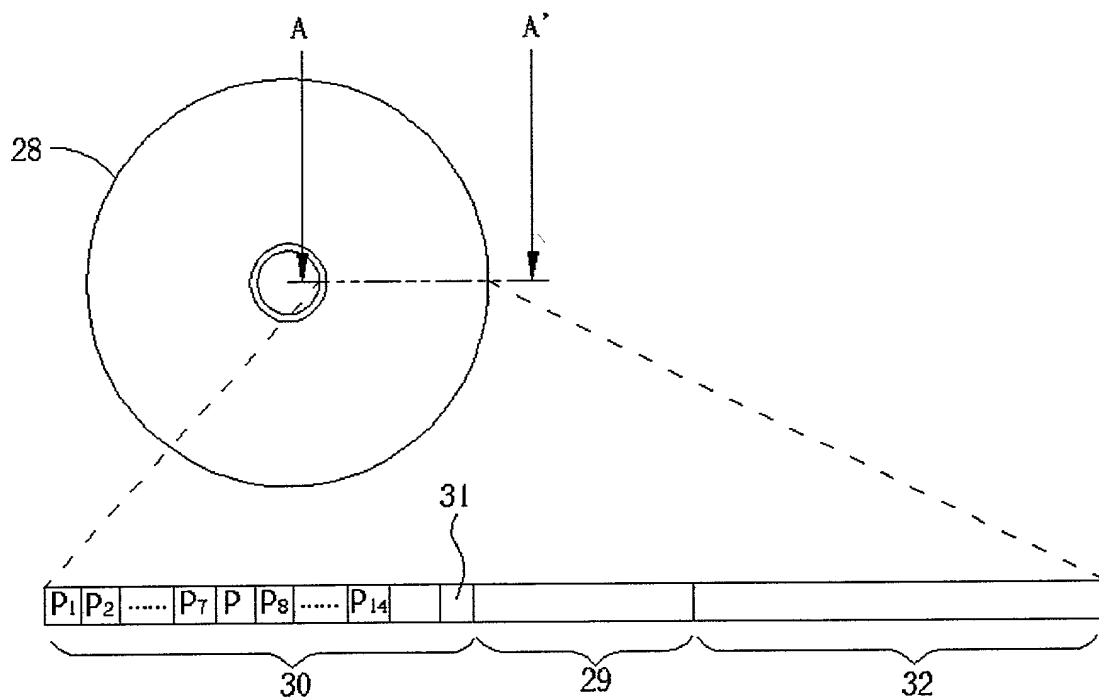


Fig. 2 Prior art

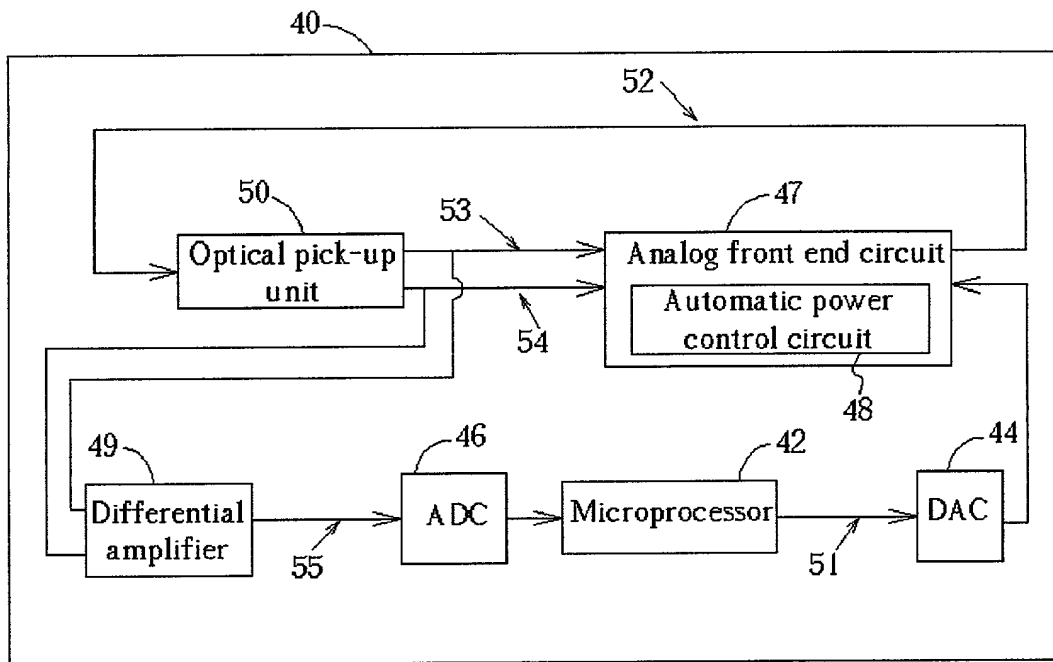


Fig. 3

Output power test values

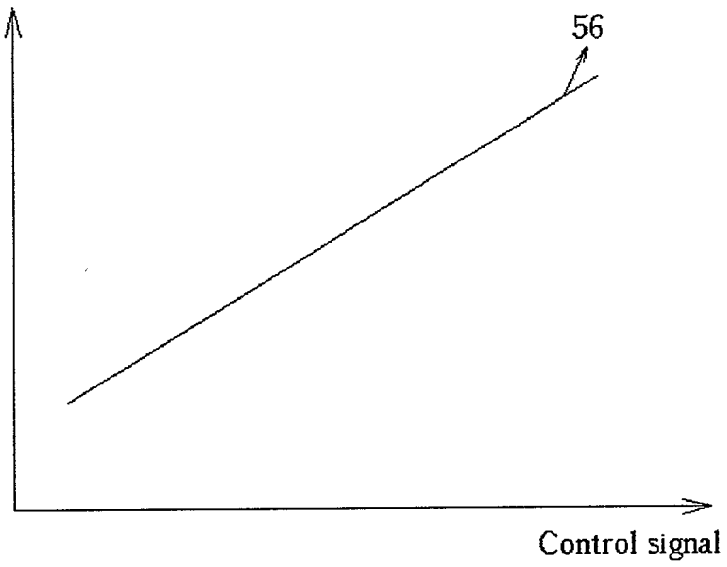


Fig. 4

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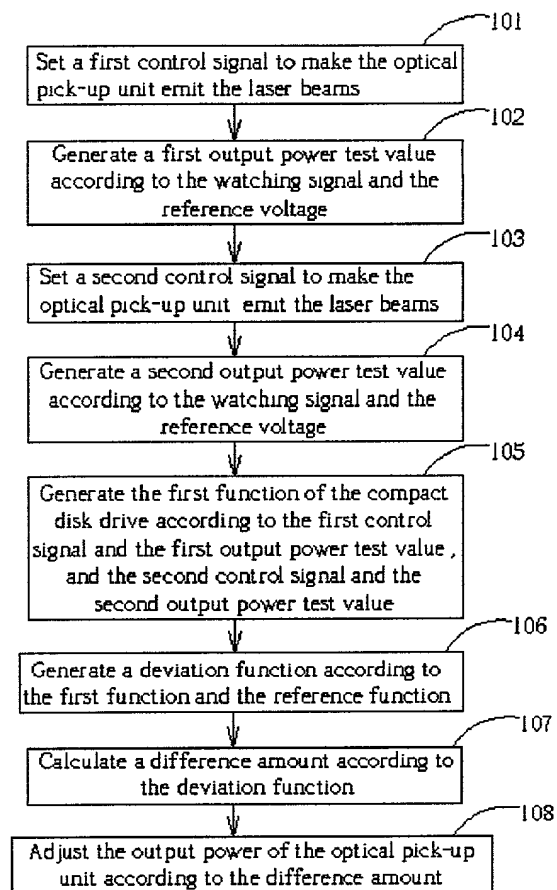
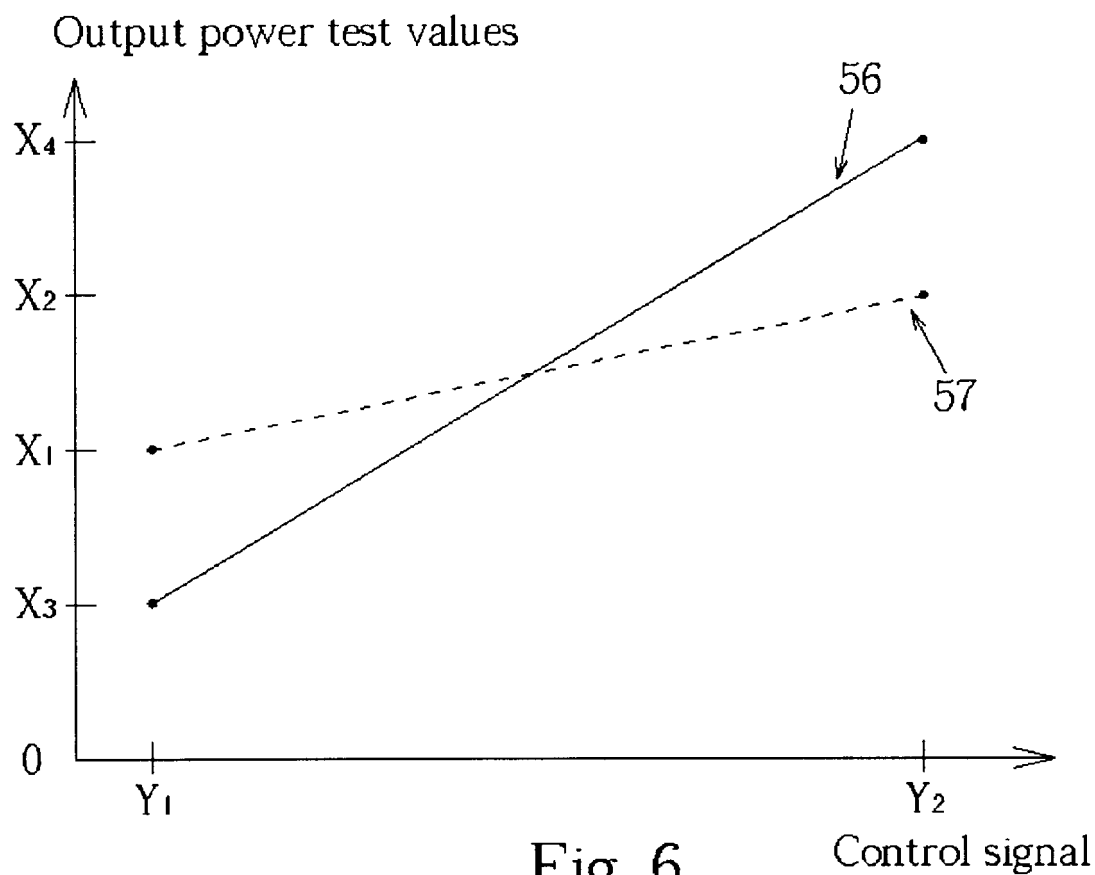


Fig. 5



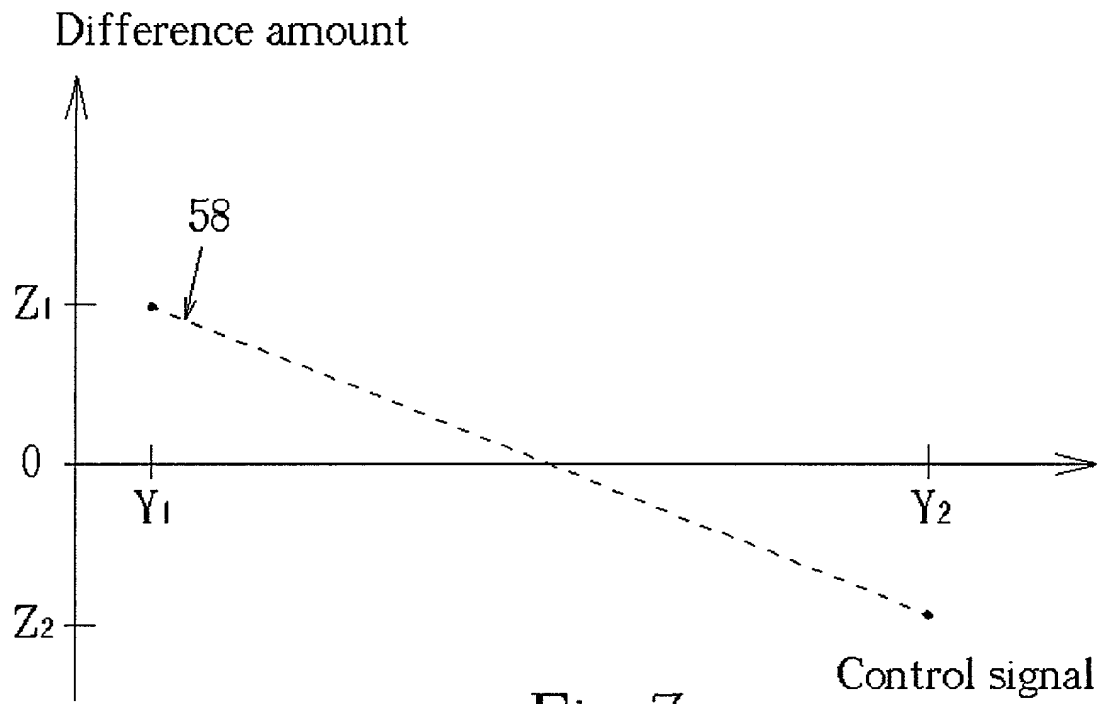


Fig. 7